

REMARKS/ARGUMENTS

Pending claims 1-4, 9-14, 19-24, 29-31, 33 and 35 stand rejected under 35 U.S.C. §102(b) over U.S. Patent No. 4,543,627 (Schwab). Applicant respectfully traverses the rejection. As to amended claim 1, nowhere does Schwab teach successively transferring data from linked buffers in a first processor system directly to linked buffers in a second processor system. This is so, at least for several reasons. First, nowhere does Schwab teach direct transfer from a first processor system to a second processor system. Instead, all data transfers in Schwab occur through an interface unit 100. Furthermore, nowhere does Schwab teach linked buffers. Instead, Schwab merely teaches that a send buffer is in a send processor 300 and a receive buffer is in a receive processor 500. Nowhere does Schwab teach the presence of multiple linked buffers within a single one of the processors. Nor does Schwab teach successive transfer of data from such (non-existent) linked buffers. For at least these reasons, claim 1 and the claims depending therefrom are patentable over Schwab. For similar reasons, independent claims 11 and 21 and the claims depending therefrom are also patentable.

Pending claims 5-6, 15-16, 25-26, 32, 34 and 36 stand rejected under 35 U.S.C. §103(a) over Schwab in view of U.S. Patent No. 5,524,134 (Gustafson). This rejection is improper at least for the same reasons discussed above regarding the independent claims from which these claims depend. Furthermore, the rejection is improper as nowhere does Gustafson teach transfer of data between buffers in a cellular phone, as contended by the Office Action. Office Action, p. 4. In this regard, nowhere does Gustafson teach or suggest the presence of buffers (and particularly not linked buffers that successively transfer data between two processor systems). Instead, all that Gustafson teaches is that a vocoder processor is coupled via a host port to an encryption engine which in turn is coupled to a programmable logic of a modem/signaling processor. Thus, Gustafson nowhere teaches or suggests successive transfer of data between linked buffers. For this further reason, the above dependent claims are further patentable.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

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Mark J. Rozman
Registration No. 42,117
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Suite 100
Houston, Texas 77024-1805
(512) 418-9944 [Phone]
(713) 468-8883 [Fax]
Customer No.: 21906